



Attorney Docket No.: 023-0029

Application No.: 10/729,865

Applicant(s): En-Hsing Chen, et al.

Filing Date: December 5, 2003

Group Art Unit: 2824

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LIST OF PENDING U. S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

- 1) U.S. Patent Application No. 10/729,844, filed December 5, 2003, entitled "NAND Memory Array Incorporating Multiple Write Pulse Programming of Individual Memory Cells and Method for Operation of Same," naming inventors En-Hsing Chen, Andrew J. Walker, Roy E. Scheuerlein, Sucheta Nallamothu, Alper Ilkbahar, and Luca G. Fasoli.
- 2) U.S. Patent Application No. 10/729,843, filed December 5, 2003, entitled "Memory Array Incorporating Memory Cells Arranged in NAND Strings," naming inventors Luca G. Fasoli, Roy E. Scheuerlein, En-Hsing Chen, Sucheta Nallamothu, Maitreyee Mahajani, and Andrew J. Walker.

Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.